Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	5623	memory and transistor and feedback and capacitor and "current source"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 14:47
S2	161	memory and transistor and feedback and capacitor and "current source" and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 14:51
S3	3	memory and transistor and (feedback near capacitor) and (current near source) and "345"/\$.ccls. and (driver near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/11 10:59
S4	2	memory and transistor and ("feedback capacitor") and (current near source) and "345"/\$.ccls. and (driver near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 14:54
S5	2	memory and transistor and ("feedback capacitor") and ("current source") and "345"/\$.ccls. and (driver near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2007/04/09 14:54
S6	2	memory and transistor and ("feedback capacitor") and ("current source") and "345"/\$.ccls. and ("driver circuit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 14:55
S7	37	memory and transistor and ("feedback capacitor") and ("current source") and ("driver circuit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 15:06
S8	42	storage and transistor and ("feedback capacitor") and ("current source") and ("driver circuit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 15:08
S9	7693	(345/204-214 or 345/87-100).ccls. and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 15:55

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S10	432	(345/204-214 or 345/87-100).ccls. and memory and "current source"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 16:19
S12	63	(345/204-214 or 345/87-100).ccls. and memory and "current source" and feedback and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 16:00
S13	4	("20020180685" or "20020000969"). pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 16:10
S14	6	("20020180685" or "20020000969" or "20040036670").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 16:10
S15	14	(345/204-214 or 345/87-100).ccls. and memory and "current source" and "slew rate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 16:41
S16	38	(345/204-214 or 345/87-100).ccls. and (memory or latch or storage) and "current source" and "slew rate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 16:49
S17	34	(345/204-214 or 345/87-100).ccls. and (memory or latch or storage) and "current source" and "slew rate" and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 19:22
S18	768	driver and (memory or latch or storage) and "current source" and "slew rate" and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 19:04
S19	346	"driver circuit" and (memory or latch or storage) and "current source" and "slew rate" and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 19:04
S20	32	"driver circuit" and (memory or latch or storage) and "current source" and "slew rate" and capacitor and ("liquid crystal" adj3 display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 19:05

S21	17	((OLIVER) near2 (LANDOLT)).INV.	US-PGPUB; USPAT	OR	ON	2007/04/09 19:17
S22	34	(345/204-214 or 345/87-100 or 345/690).ccls. and (memory or latch or storage) and "current source" and "slew rate" and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/09 19:22
S23	4	memory and transistor and (feedback near capacitor) and ("current source") and "345"/\$.ccls. and (amplifier near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/11 11:03
S24	113	memory and transistor and capacitor and ("current source") and "345"/\$. ccls. and (amplifier near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/11 11:14
S25	60	(memory or latch or storage) and transistor and capacitor and ("current source") and "345"/\$.ccls. and (amplifier near circuit) and previous and current	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/11 11:18
S26	57	(memory or latch or storage) and transistor and capacitor and ("current source") and "345"/\$.ccls. and ("amplifier circuit") and previous and current	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/11 11:31
S27	168	(memory or latch or storage) and transistor and capacitor and ("current source") and "345"/\$.ccls. and ("amplifier circuit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/11 11:38
S28	24	(memory or latch or storage) and "output transistor" and capacitor and ("current source") and "345"/\$.ccls. and ("amplifier circuit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/11 15:52
S29	8	(memory or latch or storage) and "output transistor" and capacitor and ("current source") and (345/98 or 345/100 or 345/204).ccls. and ("amplifier circuit")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/29 23:12
S30	86	(345/204-214 or 345/87-100).ccls. and memory and "current source" and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/29 22:46

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S31	18	(memory or latch or storage) and	US-PGPUB;	OR	ON	2007/04/29 23:13
		("output transistor" or "output node")	USPAT;			
		and capacitor and ("current source")	EPO; JPO;		İ	
		and (345/204-214 or 345/98 or	DERWENT;			
		345/100 or 345/204).ccls. and	IBM_TDB			
		("amplifier circuit")	_			e

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